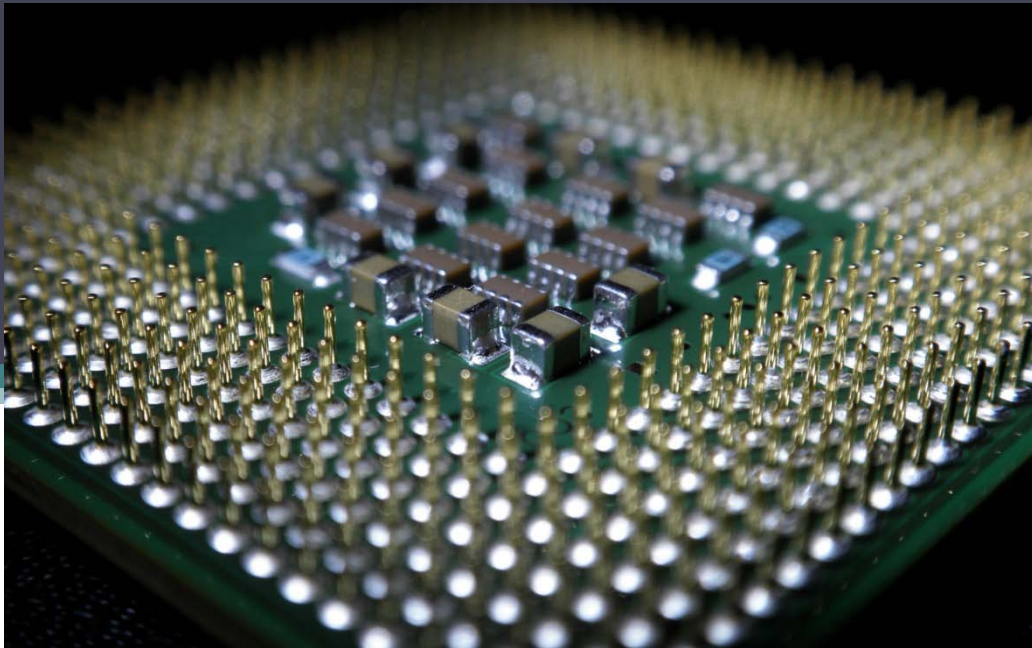
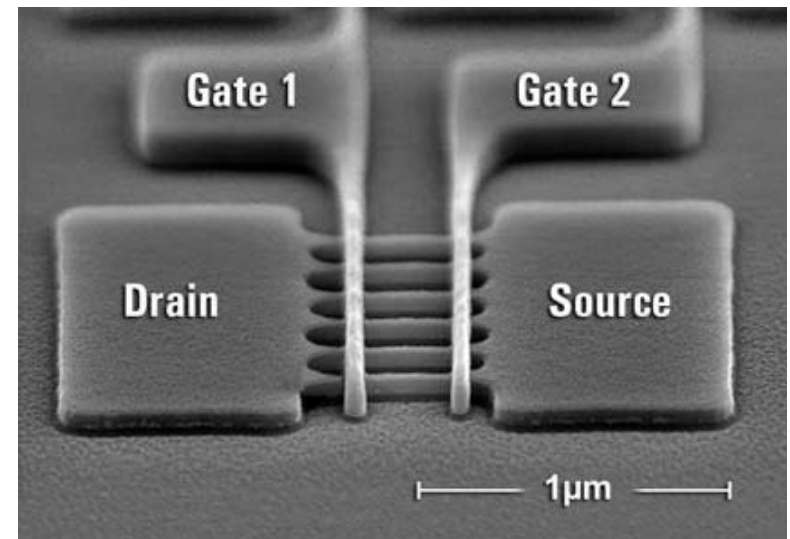
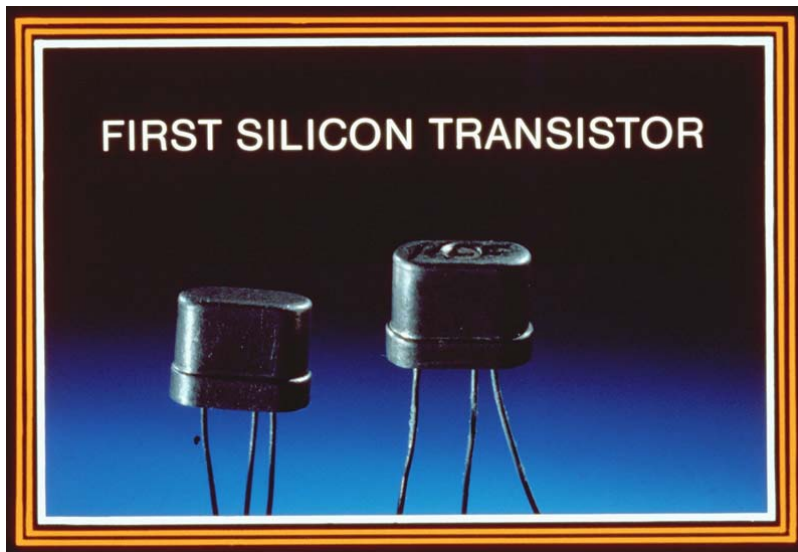


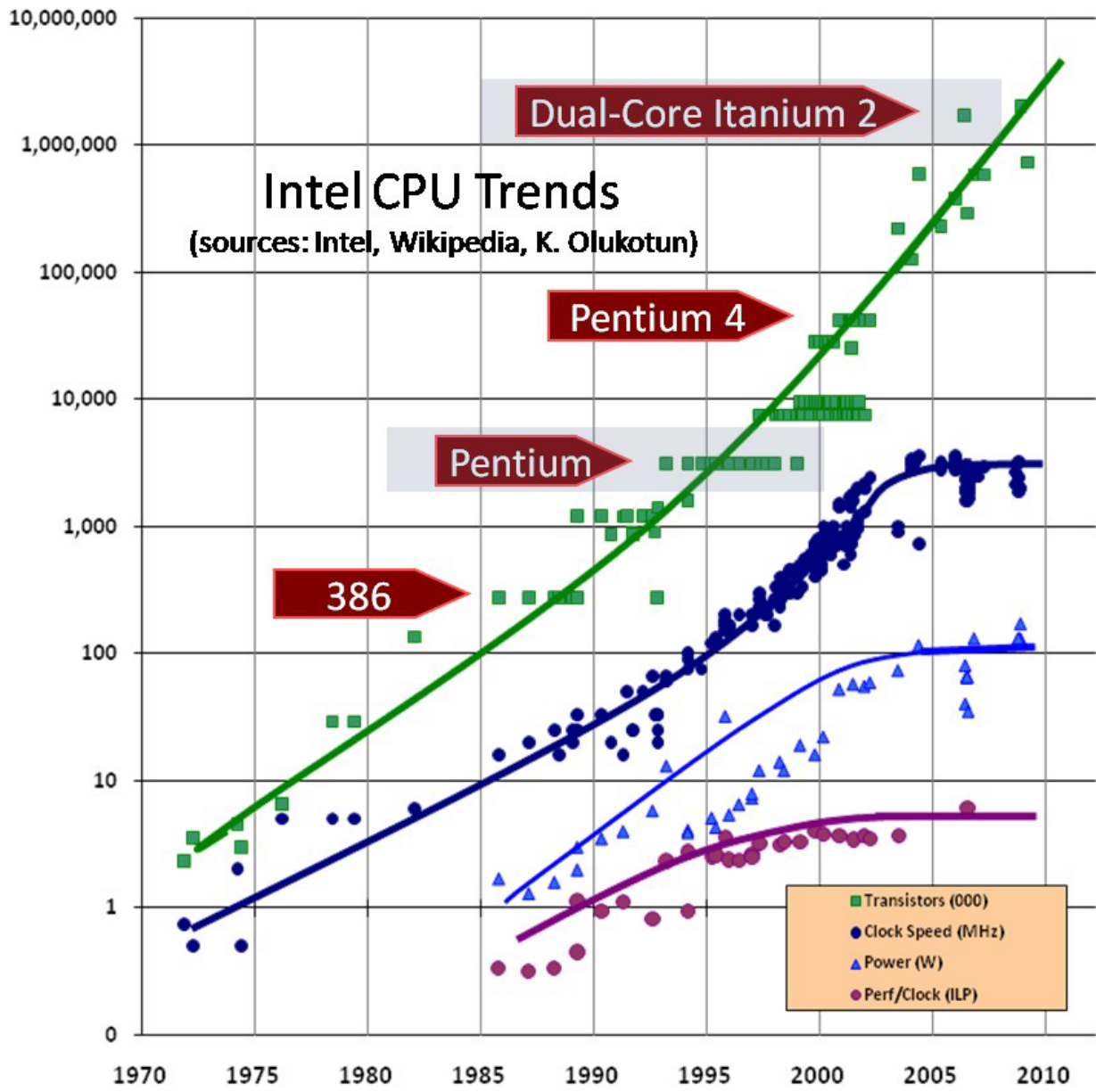
# Future of the CPU



By Jagdeep Singh

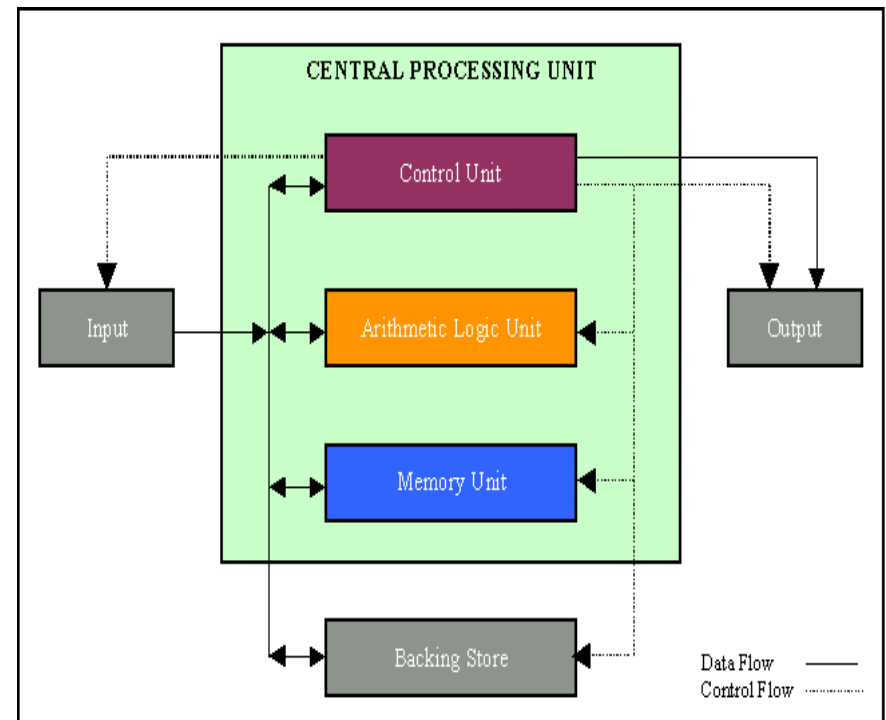
# Transistors





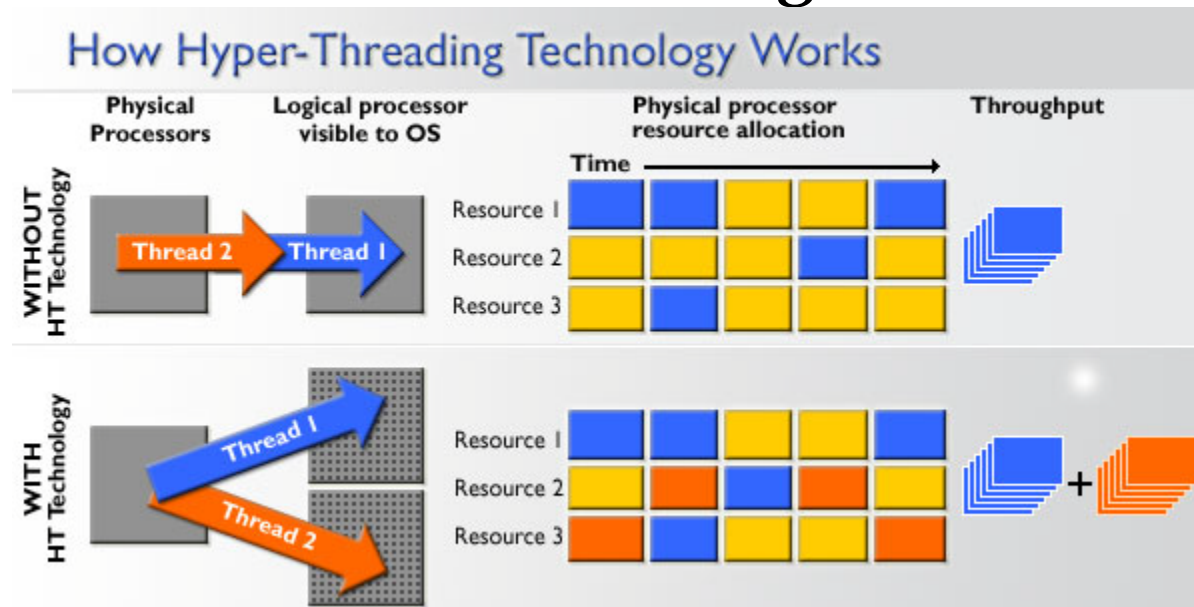
# Architecture

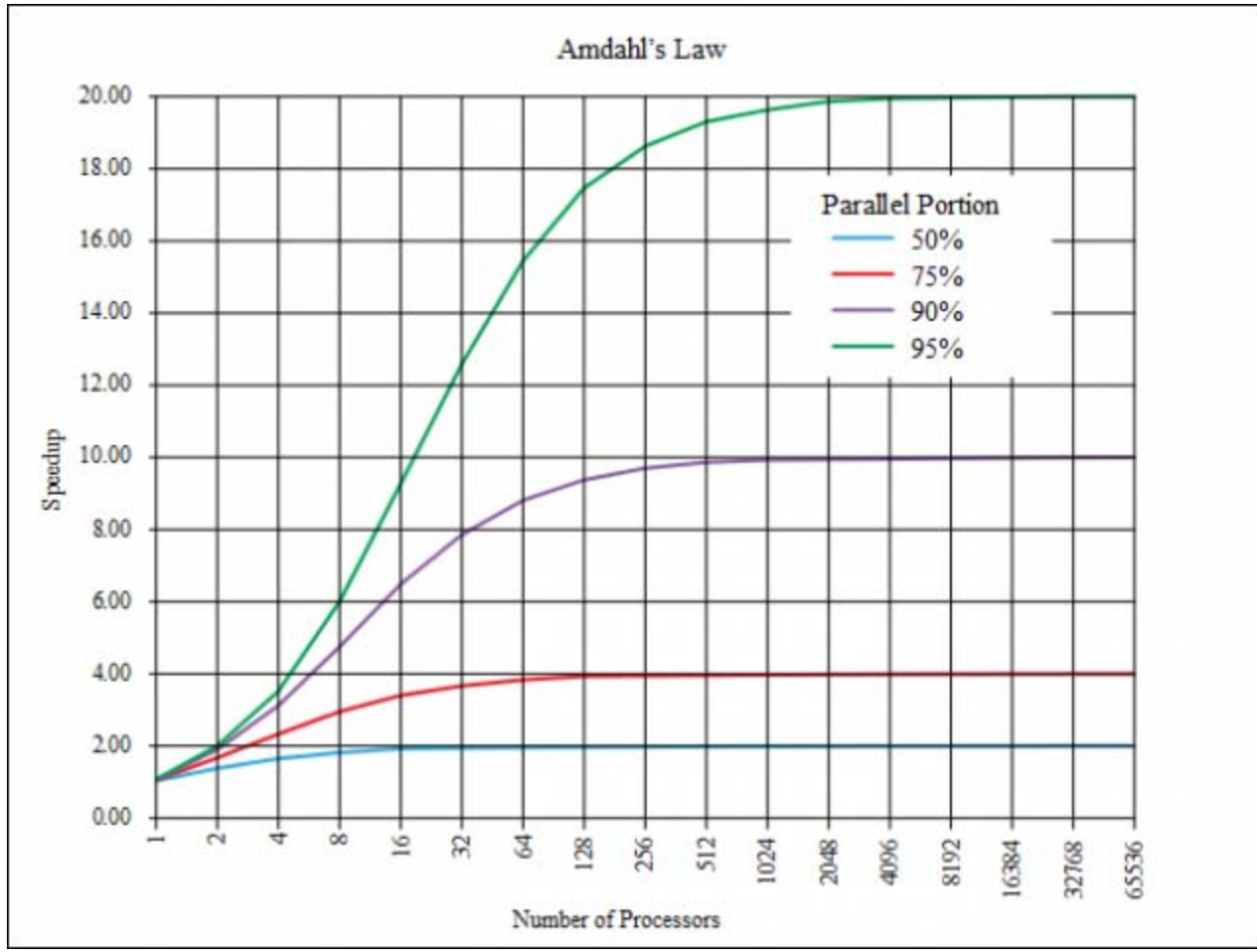
- Embedded (ARM)
- Microcomputer (x86)
- Smaller more efficient
- Cache



# Hyper threading

- Logical cores vs Physical cores
- Conveyer belt concept
- Not the same as doubling cores







# Software

- **Compiled vs Interpreted**
- **Simple is good**
- **Optimization**
- **Hardware acceleration**