

## Computers

## Logic acting on information

The Central Processing Unit (CPU)

Elements of a Computer

Computers

Logic: acting on information

The Central Processing Unit (CPU)

Elements of a Computer

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The not-or (NOR) gate


| Input $A$ | Input $B$ | Output |
| :---: | :---: | :---: |
| 1 | 1 | 0 |
| 1 | 0 | 0 |
| 0 | 1 | 0 |
| 0 | 0 | 1 |




## Computers

Logic: acting on information

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Elements of a Computer

The Central Process Unit (CPU)

CPU


The CPU consists of three parts:
the Arithmetic Logic Unit (ALU)
The Control Unit
Memory

The CPU cycles through a series of operations or instructions
The CPU cycles trrough a series of operation
organized in a cycle, the Fetch/Execute cycle

1. Instruction Fetch (IF)
2. Instruction Decode (DP)
3. Data Fetch (DF)
4. Instruction Execute (IE)
5. Result Return


Step 2: Instruction Decode


Decode instruction:
Defines operation ( $($ ) and set memory pointers in ALU
Step 3: Data Fetch
Fetch data:
Get numbers at memory positions 428 and $884: 42$ and 12
and put in ALU

Step 4: Instruction Execution


Execute:
Add numbers 42 and 12 in ALU: 54


## Possible operations

Computers can only perform about 100 different types of operations; all other operations must be broken down into simpler operations among these 100 .
Some of these operations:
-Add, Mult, Div
AND, OR, NAND, NOR,
Bits shits
Test if a
bit is 0 0
-Move information in memor

| Repeating the F/E cycle |
| :--- |
| Computers get their impressive capabilities by performing <br> many of these $\mathrm{F} / \mathrm{E}$ cycles per second. <br> The computer clock determines the rate of $\mathrm{F} / \mathrm{E}$ cycles per <br> second; itis nowexpessed in GHz , i.e. in billions of cycles <br> per seconds! <br> Note that the rate given is not an exact measurement. |

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Moores Law - The number of transistors on integrated circuit chips (1971-2016)
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(http://en.wikipedia.org/wik/Accelerating_change)


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Computer: basic scheme



Communications on the mother board


