**Today**: o Sentential Logic (continued)

#### Review:

➤ Definition of a <u>well formed formula</u> (for sentential logic, over same set of proposition symbols *P*) is:

Equivalent to a *Propositional Formula* (in the text): truth depends on the values of its *logical variables*.

> "Order of Precedence"

```
binds most tightly

v

binds most tightly

v

binds least tightly
```

Parenthesis can change the default order. Within a given precedence, the usual convention is that things group right-to-left.

For example,  $C \vee \neg A \wedge B \rightarrow B$  Is equivalent to:  $(C \vee ((\neg A) \wedge B)) \rightarrow B$ 

1. We can associate any WFF to a **tree** where the leaves are the proposition symbols and constants 0 and 1 and the **internal nodes** are marked with **logical operators**. Given a **truth assignment**, which was described last time, you can propagate up truth value to every node. We showed how to do this for the example above.

#### Some practice designing formulas .... and circuits, too

Exercise 1 Who won the fight?

Two fighters, A and B. Three judges, each votes "0" if he thinks A won and "1" if he things B won. We want to create a Boolean formula that computes who won, according to *majority vote*.

Majority(P,Q,R) = 1 iff at least two of P,Q,R are 1, and 0 otherwise.

First write out a truth table for what you want:

P	Q	R	Y
0	0	0	0
0	0	1	0
0	1	0	0
0	1	1	1
1	0	0	0
1	0	1	1
1	1	0	1
1	1	1	1

**Disjunctive normal form** (DNF) – The formula is the OR of terms, and each term is the AND of variables or their complements. We can take ANY truth table and "read it out" as DNF. Above, we get

$$(\neg P)QR \ v \ P(\neg Q)R \ v \ PQ(\neg R) \ v \ (PQR)$$

We can simplify this a bit using:

Exercise 2: Find WFF that is 1 if exactly one of A, B, C, and D, are true. (NOT DONE IN CLASS)

For exactly one of these variables to be true, we need that *at least one* of the variables is true and *at most one* of the variables is true. The first is easy to translate into sentential logic:

The second is a little trickier: we want to say if A is true, for example than B must be false, that is,  $A \rightarrow \neg B$ ; and  $A \rightarrow \neg C$ , and so forth:

So and this formula and the prior one and you are done. More generally, to say that exactly one of  $X_1, ..., X_n$  are true, we could use a formula of the form

$$(V_i X_i) \land \Lambda_{i \neq j} (X_i \rightarrow \neg X_j)$$

**Logical completeness.** By virtue of the "DNF algorithm" that turns any **truth table** to a **WFF** that corresponds to the functionality, we know that any Boolean formula—or any truth table—can be represented using only the logical connectives  $\{^{\land}, v, \neg\}$ . We say that this set of connectives are **logically complete**.

In fact, we can use a smaller set of connectives, eliminating **or** by using the identity:

$$P \vee Q \equiv \neg(\neg P \wedge \neg Q)$$

which is known as DeMorgan's law. We could, alternatively, eliminate **and** by using the DeMorgan law:

$$P \wedge Q \equiv \neg (\neg P \vee \neg Q).$$

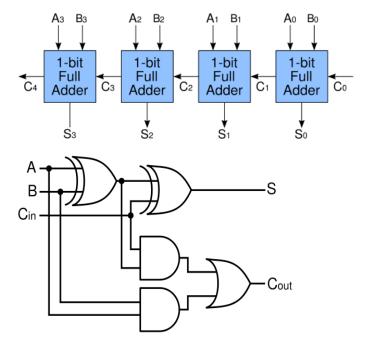
So we have show that

- ^&¬ are logically complete,
- v & ¬ are logically complete. In addition,
- NAND, all by itself, is logically complete, because we can rewrite ^ and ¬ using NAND (tie the inputs of the NAND together to make an inverter) and
- NOR, all by itself, is logically complete, because we can rewrite v and ¬ using NOR (tie the inputs of NOR together to make an inverter).

**Exercise 3**: Find a circuit to add up two 4-bit binary numbers.

Example: add 1011 (11) to 0110 (6) to get 10001 (17) showing sums and carries.

Here is a diagram for how to use a **full adder**, and then a circuit for the adder. (In class we first wrote out the truth table for the full adder.) Note that  $A_i$  and  $B_i$  are the bits of the two inputs,  $S_i$  is the current Sum bit, and  $C_i$  is the carry to go to the next place.



# Tautologies, satisfiability, and logical equivalence

### **Definitions:**

Given WFF's  $\alpha$  and  $\beta$ ,

- $\alpha$  is a **tautology** if  $\alpha$  is True for all truth assignments *t of its variables*
- $\alpha$  is **satisfiable** if there exists a truth assignment t s.t.  $\alpha$  is true.
- $\alpha$  is a **contradiction** if  $\alpha$  is False for all truth assignments t.
- $\alpha$  and  $\beta$  are (logically) **equivalent** if  $t(\alpha)=t(\beta)$  for all truth assignments t.

# **Gates Diagrams:**

Туре	Distinctive shape	Truth table		
		INPUT	OUTPUT	
AND		A B	A AND B	
		0 0	0	
		0 1	0	
		1 0	0	
		1 1	1	
OR		INPUT		
		A B	A OR B	
		0 0	0	
		0 1	1	
		1 0	1	
		1   1	1	
NOT	<b>-</b> >>-	INPUT	OUTPUT	
		A	NOT A	
		0	1	
		1	0	
		INPUT	OUTPUT	
NAND			A NAND B	
		$\begin{bmatrix} A & B \\ 0 & 0 \end{bmatrix}$	1	
		0 1	1	
		1 0	1	

		1	0	0
NOR	<b>→</b>	1NI A 0 0 1	B 0 1 0 1	OUTPUT A NOR B 1 0 0 0
XOR		00 00 11 11 11 11 11 11 11 11 11 11 11 1	0 0 1 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	0 0 1